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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER

**TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)**

10681-007

10/089532

INTERNATIONAL APPLICATION NO.
PCT/GB00/03746

INTERNATIONAL FILING DATE
29 SEP 2000

PRIORITY DATE CLAIMED
30 SEP 1999

TITLE OF INVENTION
DUAL-BAND MICROSTRIP ANTENNA

APPLICANT(S) FOR DO/EO/US
LANGLEY, Richard J., VIRATELLE, Didier

Applicant herewith submits to the United States Designated/ Elected Office (DO/EO/US) the following items under 35 U.S.C. 371:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).
4. ☒ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
 - a. ☐ is transmitted herewith (required only if not transmitted by the international Bureau).
 - b. ☒ has been transmitted by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US)
6. ☐ A translation of the International Application into English (35 U.S.C. 371(c)(2)).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))
 - a. ☒ are transmitted herewith (required only if not transmitted by the International Bureau).
 - b. ☐ have been transmitted by the International Bureaus.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☐ have not been made and will not be made.
8. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 37(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).

Items 11. to 16. below concern document(s) or information included:

11. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
12. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A **FIRST** preliminary amendment.
☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
14. ☐ A substitute specification.
15. ☐ A change of power of attorney and/or address letter.
16. ☒ Other items or information: copy of Form PCT/RO/101 (6 sheets); copy of International Application as published (24 sheets); copy of International Search Report (3 sheets); copy of Notification of Transmittal of IPER (1 sheet); copy of IPER, including amended sheets of specification and claims (21 sheets); copy of Notification Concerning Submission or Transmittal of Priority Document (1 sheet); copy of Notice Informing the Applicant of the Communication of the International Application to the Designated Offices (1 sheet); copy of Form PCT/IB/332 (1 sheet); and copy of communication from UK Patent Office (3 sheets).

INTERNATIONAL APPLICATION NO.
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17. ☒ The U.S. National Fee (35 U.S.C. 371(c)(1)) and other fees as follows:

CLAIMS				
(1)FOR	(2)NUMBER FILED	(3)NUMBER EXTRA	(4)RATE	(5)CALCULATIONS
TOTAL CLAIMS	19 -20	0	X \$18.00	\$ 0.00
INDEPENDENT CLAIMS	3 -3	0	X \$84.00	0.00
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			+ \$280.00	<input type="checkbox"/>
BASIC NATIONAL FEE (37 CFR 1.492(a)(1)-(5)): CHECK ONE BOX ONLY				
<input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482) \$710.00				
<input type="checkbox"/> No international preliminary examination fee paid to USPTO (37 CFR 1.482) but international search fee paid to USPTO (37 CFR 1.445(a)(2)) \$740.00				
<input type="checkbox"/> Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO \$1,040.00				
<input type="checkbox"/> International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(2) to (4) \$100.00				
<input checked="" type="checkbox"/> Filing with EPO or JPO search report \$890.00				\$ 890.00
Surcharge of \$130.00 for furnishing the National fee or oath or declaration later than 20 30 mos. from the earliest claimed priority date (37 CFR 1.492(e)).				
0			TOTAL OF ABOVE CALCULATIONS	= 890.00
Reduction by 1/2 for filing by small entity, if applicable. Affidavit must be filed also. (Note 37 CFR 1.9, 1.27, 1.28).				- \$ 0.00
			SUBTOTAL	= 890.00
Processing fee of \$130.00 for furnishing the English Translation later than 20 30 mos. from the earliest claimed priority date (37 CFR 1.492(f)).				+
0			TOTAL FEES ENCLOSED	\$ 890.00

- a. ☐ A check in the amount of \$ 890 to cover the above fees is enclosed.
- b. ☒ Please charge Deposit Account No. 16-1150 in the amount of \$ 890 to cover the above fees. A copy of this sheet is enclosed.
- c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 16-1150. A copy of this sheet is enclosed.

18. ☐ Other instructions
n/a

19. ☒ All correspondence for this application should be mailed to
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REGISTRATION NUMBER

March 28, 2002
DATE

10/089532

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Express Mail No.: EL 477 035 276 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Langley et al.

Application No.: Not yet assigned -
National stage of
PCT/GB00/03746

Group Art Unit: Not yet assigned

Filed: Herewith

Examiner: Not yet assigned

For: DUAL-BAND MICROSTRIP
ANTENNA

Attorney Docket No.: 10681-007

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please cancel claims 1-19, and add claims 20-38:

20. A dual-band microstrip antenna comprising:
a ground member; and
a patch structure having discrete first and second portions that are generally parallel to each other and spaced apart from the ground member, the patch structure and the ground member being configured such that the antenna exhibits first and second resonant frequency ranges by electromagnetic interaction between the patch structure and the ground member when the antenna is active; wherein conduction surfaces of the portions of the patch structure are shaped to substantially correspond to patterns of current flow detected in the conduction surfaces when the antenna is active before such shaping.

21. A dual-band microstrip antenna as in claim 20, wherein conduction surfaces of the ground member are shaped to substantially correspond to patterns of current flow detected in the ground-member conduction surfaces when the antenna is active before such shaping.

22. A dual-band microstrip antenna as in claim 20, wherein the ground member has a rectangular outer profile and wherein sides and one end of the patch structure are in respective alignment with sides and one end of the ground member.

23. A dual-band microstrip antenna as in claim 22, wherein the first portion of the patch structure is a first patch, wherein the second portion of the patch structure is a pair of second patches each positioned adjacent a respective opposite side of the first patch, one end of each first and second patch corresponding to the one end of the patch structure, wherein an antenna signal feedline is connected to a generally central position on the first patch, and wherein a shorting member extends from each second patch to the ground member at a point proximate the one end of the second patch and the ground member.

24. A dual-band microstrip antenna comprising:
a ground member; and
first and second portions of a patch structure that is in a generally parallel spaced relationship with the ground member, first and second resonant frequency ranges being defined by electromagnetic interaction between the patch structure and the ground member; wherein sides and one end of the patch structure are in respective alignment with aides and one end of the ground member, wherein the first portion of the patch structure is a first patch and the second portion of the patch structure is a pair of second patches, each second patch having a side adjacent a respective opposite side of the first patch, one end of each first and second patch corresponding to the one end of the patch structure, wherein an antenna signal feedline is connected to a generally central position on the first patch, wherein the first patch is not directly connected to the ground member, and wherein a shorting member extends from each second patch to the ground member at a point proximate the one end of the second patch and the ground member.

25. A dual-band microstrip antenna as in claim 23, wherein each second patch has a length approximating the length of the first patch, and has a width approximating one-half the width of the first patch.

26. A dual-band microstrip antenna as in claim 24, wherein each second patch has a length approximating the length of the first patch, and has a width approximating one-half the width of the first patch.

27. A dual-band microstrip antenna as in claim 25, wherein the first patch is generally configured as an 'H', with the sides of the first patch corresponding to side members of the 'H'.

28. A dual-band microstrip antenna as in claim 26, wherein the first patch is generally configured as an 'H' with the sides of the first patch corresponding to side members of the 'H'.

29. A dual-band microstrip antenna as in claim 23, wherein a conduction surface of the ground member is configured as a hollow generally rectangular structure, with a cross-piece extending between the sides of the structure at a projection of the position at which the antenna signal feedline connects to the first patch.

30. A dual-band microstrip antenna as in claim 27, wherein a conduction surface of the ground member is defined by two side members and an other-end member and with a cross-piece extending between the two side members at a projection of the position at which the antenna signal feedline connects to the first patch, and wherein extensions of the side members of the first patch extend from the one end of the patch structure to the plane of the ground member and then in the plane of the ground member for a part of the distance toward the cross-piece.

31. A dual-band microstrip antenna as in claim 28, wherein a conducting surface of the ground member is defined by two side members and an other end member and with a

cross-piece extending between the two side members at a projection of the position at which the antenna signal feedline connects to the first patch, and wherein extensions of the side members of the first patch extend from the one end of the patch structure to the plane of the ground member and then in the plane of the ground member for a part of the distance toward the cross-piece.

32. A dual-band microstrip antenna as in claim 29, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal feed portion of the cable defines the antenna signal feedline attached to the first patch.

33. A dual-band microstrip antenna as in claim 30, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal feed portion of the cable defines the antenna signal feedline attached to the first patch.

34. A dual-band microstrip antenna as in claim 31, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal feed portion of the cable defines the antenna signal feedline attached to the first patch.

35. A dual-band microstrip antenna as in claim 29, wherein the antenna is formed from a printed circuit board having a conductive layer on one side, wherein conducting surfaces of the ground member are formed by removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein conducting surfaces of the patch structure are formed by removing portions of a conductive layer on the one side of a second segment of the circuit board, and wherein the first and second segments of the circuit board are then mounted in parallel spaced relationship, and shorting members are applied between the ground member and the second patches proximate the one end of the ground member and the second patches.

36. A dual-band microstrip antenna as in claim 30, wherein the antenna is formed from one or more printed circuit boards having a conductive layer on one side, wherein the conducting surfaces of the ground member are formed by removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein the conducting surfaces of the patch structure are formed by removing portions of the conductive layer on the one side of a second segment of the circuit board, wherein the first and second segments of the circuit board are then mounted in parallel spaced relationship, and wherein shorting members are applied between the one end of the ground member and the one end of the first and second patches.

37. A dual-band microstrip antenna as in claim 31, wherein the antenna is formed from one or more printed circuit boards having a conductive layer on one side, wherein the conducting surfaces of the ground member are formed by removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein the conducting surfaces of the patch structure are formed by removing portions of the conductive layer on the one side of a second segment of the circuit board, wherein the first and second segments of the circuit board are then mounted in parallel spaced relationship, and wherein shorting members are applied between the one end of the ground member and the one end of the first and second patches.

38. A dual-band microstrip antenna comprising at least two conductive radiating structures having electromagnetic interaction, at least one of the structures being apertured at locations where, if apertures were not present, induced currents would be relatively low compared to currents in other parts of the structure.

[illegible]

In view of the above Amendment, Applicants believe the subject application has been placed in condition for allowance, and such action is respectfully requested. Please charge appropriate fees, if any, to Deposit Account 16-1150.

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DUAL-BAND MICROSTRIP ANTENNA

The invention relates to a dual-band antenna, and provides a dual-band microstrip antenna that has ground and patch elements configured such that the contour of the surface areas of the elements substantially corresponds to the pattern of induction currents created in the elements by signals in the dual bands.

One important use of dual-band microstrip antennas is in mobile communication systems. A common configuration for an antenna in such use is the inverted-F geometry which is described in two articles by Zi Dong Liu and Peter S. Hall. The first article is "Dual-band antenna for hand held portable telephones", Electronics Letters, Vol. 32, No. 7, pp. 609-610 (March 1996), and the second (and more comprehensive) is "Dual-Frequency Planar Inverted-F Antenna", IEEE Transactions on Antennas and Propagation, Vol. 45, pp. 1451-1457 (October 1997).

Liu and Hall describe two dual-frequency-band antenna configurations, one with a single input port and the other with two input ports. The two-port antenna consists of two co-planar radiating elements -- the first one being rectangular and the second one being L-shaped and having two sides adjacent the first one. The rectangular element is for 1.8 GHz signals, while the L-shaped element is for 0.9 GHz signals. This configuration of dual-band antenna is about the same size as a single-band inverted-F antenna for 0.9 GHz signals. Both the rectangular element and the L-shaped element have one end shorted to the ground plane. Because the two radiating elements are not connected, the coupling between the two antennas is small and only due to fringe-field interaction. A variation has a single input port connected to an intermediate point of connection between the rectangular element and the L-shaped element. Although it has the advantage of using only a single input port, this arrangement has the drawback that the coupling

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between the rectangular element and the L-shaped element is increased.

As with the variation of the dual-frequency-band antenna of Lui and Hall, the antenna of the subject invention utilizes multiple radiating elements with a single input port; unlike the antenna of Lui and Hall, however, the multiple radiating elements of the subject antenna are not connected. The antenna of the subject invention has the advantages over that of Lui and Hall of having only two shorting points and a much-increased bandwidth. In addition, portions of the radiating and ground elements that carry little or no surface current are removed, resulting in weight reduction and a degree of transparency. A further advantage is that the dual-band antenna of the invention is capable of being mass-produced at low cost using flexible printed circuit board.

U.S. Patent 5,365,246 (Siemens Aktiengesellschaft) discloses a transmitting and/or receiving arrangement for portable appliances. In one embodiment (Figure 5), three sheet-metal angles 2, 9 and 3 extend from a connected shielding housing 1, and a signal feedline 4 connects with the central one of the angles (angle 9). This arrangement differs from the subject invention, in which the central patch is not connected to ground.

An article by Y.K. Cho et al., entitled "Improved Analysis Method for Broadband Rectangular Microstrip Antenna Geometry Using E-plane Gap Coupling", Electronic Letters, Vol. 29, No. 22 (28 October 1993), discloses an antenna having a ground member and capacitively-coupled short-circuited parasitic outer patches at the radiating edges of a central patch, as shown in Figure 3 of that article. This reference refers to analysis of coupling slots used to improve bandwidth of microstrip antennas. This antenna operates in only a single band, and the coupling is used to improve bandwidth of that band. Each outer patch is short-circuited to ground along the full

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length of one side edge, which is opposite the other side edge that abuts the central patch. This differs from the subject invention in which the the pair of outer patches are each shorted to ground through one end.

5 In one form, the invention is a dual-band microstrip antenna that includes a ground member and also includes a patch means having discrete first and second portions which are generally parallel to each other and spaced
10 apart from the ground member. The patch means and the ground member are configured such that the antenna exhibits first and second resonant frequency ranges by electromagnetic interaction between the patch means and the ground member when the antenna is active. Conduction
15 surfaces of the portions of the patch means are shaped to substantially correspond to patterns of current flow detected in the conduction surfaces when the antenna is active before such shaping. Conduction surfaces of the ground member may be shaped in a similar manner.

In the antenna, sides and one end of the patch means
20 may be in respective alignment with sides and one end of the ground member. The first portion of the patch means may be a first patch, and the second portion of the patch means may be a pair of second patches each having a side adjacent a respective opposite side of the first patch.
25 One end of each first and second patch corresponds to the one end of the patch means. An antenna signal feedline is connected to a generally central position on the first patch. The first patch is not directly connected to the ground member, and a shorting member extends from each
30 second patch to the ground member at a point proximate the one end of the second patch and the ground member.

Each second patch may have a length approximating the length of the first patch, and a width approximating one-half the width of the first patch. The first patch may be
35 generally configured as an 'H', with the sides of the first patch corresponding to side members of the 'H'.

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In a first construction, the conduction surfaces of the ground member may be configured as a hollow generally rectangular structure, with a cross-piece extending
5 between the sides of the structure at a projection of the position at which the antenna signal feedline connects to the first patch. In a second construction, the conduction surfaces of the ground member may be defined by two side members and an other-end member and with a cross-piece
10 extending between the two side members at a projection of the position at which the antenna signal feedline connects to the first patch. In the second construction, extensions of the side members of the first patch extend from the one end of the patch means to the plane of the ground
15 member and then in the plane of the ground member for a part of the distance toward the cross-piece.

A coaxial cable may be attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal
20 feed portion of the cable defines the antenna signal feedline attached to the first patch.

The antenna may be formed from printed circuit board having a conductive layer on one side. The conducting surfaces of the ground member are formed by removing
25 portions of a conductive layer on the one side of a first segment of the circuit board. The conducting surfaces of the patch means are formed by removing portions of the

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conductive layer on the one side of a second segment of the board. The first and second segments of the circuit board are then mounted in parallel spaced relationship. In the first construction, shorting members are applied
5 between the ground member and the second patches proximate the one end of the ground member and the second patches, whereas in the second construction, shorting members are applied between the one end of the ground member and the one end of the first and second patches.

10 In another form, the invention is a dual-band microstrip antenna that includes a ground member and first and second portions of a patch means. The patch means is in a generally parallel spaced relationship with the ground member. First and second resonant frequency ranges are
15 defined by the electromagnetic interaction between the patch means and the ground member. Sides and one end of the patch means are in respective alignment with sides and one end of the ground member. The first portion of the patch means is a first patch, and the second portion of
20 the patch means is a pair of second patches each positioned adjacent a respective opposite side of the first patch. One end of each first and second patch corresponds to one end of the patch means. An antenna signal feedline is connected to a generally central position on the first
25 patch, and a shorting member extends from each second patch to the ground member at a point proximate the one end of the second patch and the ground member.

The invention will next be more fully described by way of example only, by means of preferred embodiments,
30 utilizing the accompanying drawings, in which:

Figure 1 is a perspective view of a typical prior art inverted-F antenna adapted to operate over a single frequency band;

Figure 2 is a perspective view of an embodiment of
35 the dual-band microstrip antenna of the invention;

Figure 3 is an illustration of the surface currents

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on the antenna of Figure 2 at a radiating frequency of 925 Megahertz;

Figure 4 is an illustration of the surface currents on the antenna of Figure 2 at a radiating frequency of
5 1800 Megahertz;

Figure 5 is a perspective view of another embodiment of the microstrip antenna of the invention, the antenna being similar to Figure 2 but having excess metal removed from the ground plate and the patch plates;

10 Figure 6 is a further embodiment of the microstrip antenna of the invention, the antenna being a slightly-modified version of the antenna of Figure 5;

Figure 7 is a top view of a ground plate of the further embodiment of the antenna of the invention;

15 Figure 8 is a top view of the patches of the further embodiment of the antenna of the invention;

Figure 9 is a side or cross view of the further embodiment of the microstrip antenna of the invention;

20 Figure 10 is a graph illustrating the return loss for the antennas shown in Figures 2 and 5;

Figure 11 is an illustration of the radiation patterns obtained in the YZ plane (based on the axes orientation shown in Figure 2) for the antenna of the embodiment shown in Figures 6 to 9, measured at 925 MHz
25 and 1800 MHz;

Figure 12 is an illustration of the radiation patterns obtained in the XZ plane (based on the direction of axes shown in Figure 2) for the antenna of the embodiment shown in Figures 6 to 9, measured at 925 MHz
30 and 1800 MHz; and,

Figure 13 is an illustration of a further embodiment of the dual-band antenna of the invention, that antenna having a wrap-around first patch.

Referring first to Figure 1, the typical prior art
35 inverted-F antenna operating on a single frequency band has a ground plate 20 of length L that is connected to a

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patch plate 22 of length P through a shorting plate 24 of height H; the three plates 20, 22 and 24 all have a width W. A feed pin 26, which is an extension of the centre wire of a coaxial cable (not shown) that has its ground wire connected to ground plate 20, connects to a central position on the patch plate 22. The length P of patch plate 22 approximates one-quarter wavelength at the mid-range of the frequency band of the antenna. The metallic surface of ground plate 20 may be provided by the metallic side of a portable telephone or other device on which the antenna is used.

Prior to removal of metal from the ground plate and the radiating patches, an embodiment of the dual-band microstrip antenna has, as shown in Figure 2, a ground plate 30, a central patch plate 32, a pair of side patch plates 34, and a pair of shorting strips 36 each of which connects a respective side patch plate 34 to the ground plate 30. A feed pin 38, which as with feed pin 26 in Figure 1 is an extension of the centre wire of a coaxial cable (not shown), connects to a central position on the central patch plate 32; a ground wire of the coaxial cable is connected to the ground plate 30. The connection point of feed pin 38 and the lengths of patch plates 32 and 34 are experimentally adjusted until the desired antenna bandwidths and a 50-ohm impedance match with the coaxial cable are obtained. As shown, the side patch plates 34 are each narrower and slightly shorter than the central patch plate 32. Figure 2 illustrates the orientation of the antenna with respect to a X-Y-Z co-ordinate system that has application to the radiation patterns shown in Figures 11 and 12.

When the surface currents on conductive material of the antenna of Figure 2 were measured in the frequency ranges of 925 MHz (Figure 3) and 1800 MHz (Figure 4), it was found that little or no surface current was present on large areas of the conductive material at either frequency

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range. Those areas of the conductive material therefore contribute to the weight but not to the performance of the antenna, and may be removed. Removal of that material has also been found to improve the bandwidth.

5 Figure 5 illustrates the antenna of Figure 2 after removal of the conductive material that was found to carry little or no surface current in the two frequency bands of interest. A central portion of ground plate 30 has been removed except for a cross-piece 40 to which a signal
10 carrier, such as a coaxial cable, is connected. Two central sections of the central patch plate 32 have also been removed -- giving the central patch plate 32 an 'H' configuration.

 The embodiment of the antenna in Figure 6 differs
15 from the one shown in Figure 5 in the type and placement of the shorting means; except for the shorting means, the numbering of parts in both is the same. The shorting means differs between the embodiments of Figures 5 and 6 in that each shorting pin 42 in the Figure 6 embodiment is
20 not connected between the end of the ground plate 30 and the end of a respective side patch plate 34, but instead is connected at positions removed from the ends. Each shorting pin 42 extends (as shown in Figures 7 and 8)
25 between a hole 44 on ground plate 30 and a hole 46 on a respective side patch plate 34. The signal feed pin 38 extends through the large hole 48 in cross-piece 40. A top view of the ground conducting plate is shown in Figure 7, and a top view of the patch plates is shown in Figure 8. A side or cross view of the antenna of Figure 6 is
30 shown in Figure 9, in which a connector 50 for connecting a coaxial cable or other signal carrier to the ground plate 30 is shown.

 The numbers adjacent the arrows in Figures 7 to 9 represent in millimetres the dimensions of the ground
35 plate 30 and the patch plates 32, 34 in the antenna of this preferred embodiment -- as well as their relative

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spacing. The ground plate is 13.5 cm. long and 20 cm. wide, the central patch plate 32 is 86.75 mm. long and 8 mm. wide, and the side patch plates 34 are each 82 mm. long and 3 mm. wide. The width of the spacing between the central patch plate 32 and each side patch plate 34 is 2 mm. Each of the holes 44 and 46, to which the shorting pins 42 connect, is 12 mm. from the end of the respective ground plate 30 and side patch plate 34.

Figure 10 illustrates the difference in return loss between the antennas of Figures 2 and 5. At the two resonant frequencies, the return loss can be seen to be greater in the antenna with metal removed (solid line) than in the antenna without metal removed (dotted line). Measured radiation patterns in the YZ and XZ planes (with reference to the co-ordinate system in Figure 2) for the antenna embodiment of Figures 6 to 9 are shown in Figures 11 and 12, respectively.

Figure 13 illustrates a further preferred embodiment of the antenna of the invention. It differs from the embodiment shown in Figures 6 to 9 in that the central patch plate 32 has a wrap-around configuration in which one end of a hollow ground plate 30 has been removed, and the sides of the central patch plate 32 have been extended across to the plane of the ground plate 30 and then a part of the distance toward the cross-piece 40 in that plane.

A dual-band microstrip antenna has a ground plate and also has a central patch positioned between a pair of side patches. The antenna has a single signal feedline, connected to the central patch, and the side patches are shorted to the ground element. Conductive surfaces of the ground plate and patches that carry surface current from signal radiation are contoured such that only portions of conductive surfaces that carry more than a negligible amount of the surface current are retained. The antenna has a reduced weight and improved bandwidth over conventional antennas that operate in the 925 MHz and 1800

JUL-2002 11:13 FROM JCH

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1. The first part of the document is a list of names and titles, including "The Hon. Mr. Justice" and "The Hon. Mr. Justice".

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ΔΙΕΥΘΥΝΣΗ ΕΠΙΧΕΙΡΗΣΕΩΝ Ε.Α.Ε.Α. 2 ΙΑΝ 19.11

CLAIMS:

1. A dual-band microstrip antenna comprising:
a ground member (30); and,
patch means having discrete first and second portions
(32, 34) which are generally parallel to each other and
spaced apart from the ground member, the patch means and
the ground member being configured such that the antenna
exhibits first and second resonant frequency ranges by
electromagnetic interaction between the patch means and
the ground member when the antenna is active;
wherein conduction surfaces of the portions (32, 34) of
the patch means are shaped to substantially correspond to
patterns of current flow detected in the conduction
surfaces when the antenna is active before such shaping.
2. An dual-band microstrip antenna as in claim 1,
wherein conduction surfaces of the ground member (30) are
shaped to substantially correspond to patterns of current
flow detected in the ground-member conduction surfaces
when the antenna is active before such shaping.
3. A dual-band microstrip antenna as in claim 1 or
2, wherein the ground member (30) has a rectangular outer
profile and wherein sides and one end of the patch means
are in respective alignment with sides and one end of the
ground member (30).
4. A dual-band microstrip antenna as in claim 3,
wherein the first portion of the patch means is a first
patch (32), wherein the second portion of the patch means
is a pair of second patches (34) each positioned adjacent
a respective opposite side of the first patch, one end of
each first and second patch corresponding to the one end
of the patch means, wherein an antenna signal feedline

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(38) is connected to a generally central position on the first patch (32), and wherein a shorting member (36) extends from each second patch (34) to the ground member (30) at a point proximate the one end of the second patch (34) and the ground member (30).

5. A dual-band microstrip antenna comprising:
a ground member (30); and,
first and second portions (32, 34) of a patch means that is in a generally parallel spaced relationship with the ground member (30), first and second resonant frequency ranges being defined by electromagnetic interaction between the patch means and the ground member; wherein sides and one end of the patch means are in respective alignment with sides and one end of the ground member (30), wherein the first portion of the patch means is a first patch (32) and the second portion of the patch means is a pair of second patches (34), each second patch (34) having a side adjacent a respective opposite side of the first patch (32), one end of each first and second patch corresponding to the one end of the patch means, wherein an antenna signal feedline (38) is connected to a generally central position on the first patch (32), wherein the first patch is not directly connected to the ground member, and wherein a shorting member (36) extends from each second patch to the ground member at a point proximate the one end of the second patch and the ground member.

6. A dual-band microstrip antenna as in claim 4, wherein each second patch (34) has a length approximating the length of the first patch (32), and has a width approximating one-half the width of the first patch.

7. A dual-band microstrip antenna as in claim 5, wherein each second patch (34) has a length approximating

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the length of the first patch (32), and has a width approximating one-half the width of the first patch.

8. A dual-band microstrip antenna as in claim 6, wherein the first patch (32) is generally configured as an 'H', with the sides of the first patch corresponding to side members of the 'H'.

9. A dual-band microstrip antenna as in claim 7, wherein the first patch (32) is generally configured as an 'H', with the sides of the first patch corresponding to side members of the 'H'.

10. A dual-band microstrip antenna as in claim 4, wherein the conduction surfaces of the ground member (30) is configured as a hollow generally rectangular structure, with a cross-piece extending between the sides of the structure at a projection of the position at which the antenna signal feedline (38) connects to the first patch.

11. A dual-band microstrip antenna as in claim 8, wherein the conduction surface of the ground member (30) is defined by two side members and an other-end member and with a cross-piece extending between the two side members at a projection of the position at which the antenna signal feedline (38) connects to the first patch (32), and wherein extensions of the side members of the first patch extend from the one end of the patch means to the plane of the ground member and then in the plane of the ground member for a part of the distance toward the cross-piece.

12. A dual-band microstrip antenna as in claim 9, wherein the conducting surface of the ground member (30) is defined by two side members and an other end member and with a cross-piece extending between the two side members at a projection of the position at which the antenna

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signal feedline (38) connects to the first patch (32), and wherein extensions of the side members of the first patch extend from the one end of the patch means to the plane of the ground member and then in the plane of the ground
10 member for a part of the distance toward the cross-piece.

13. A dual-band microstrip antenna as in claim 10, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal feed portion of the cable defines the antenna signal feedline (38) attached to the first patch (32).

14. A dual-band microstrip antenna as in claim 11, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal feed portion of the cable defines the antenna signal feedline (38) attached to the first patch (32).

15. A dual-band microstrip antenna as in claim 12, wherein a coaxial cable is attached to the antenna such that a ground portion of the cable is connected to the cross-piece of the ground member, and such that a signal
5 feed portion of the cable defines the antenna signal feedline (38) attached to the first patch (32).

16. A dual-band microstrip antenna as in claim 10, wherein the antenna is formed from printed circuit board having a conductive layer on one side, wherein the
10 conducting surfaces of the ground member (30) are formed by removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein the conducting surfaces of the patch means (32, 34) are formed by removing portions of the conductive layer on the one
15 side of a second segment of the circuit board, and wherein

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the first and second segments of the circuit board are then mounted in parallel spaced relationship, and shorting members (36) are applied between the ground member (30) and the second patches (34) proximate the one end of the
20 ground member and the second patches.

17. A dual-band microstrip antenna as in claim 11, wherein the antenna is formed from printed circuit board having a conductive layer on one side, wherein the conducting surfaces of the ground member are formed by
5 removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein the conducting surfaces of the patch means (32, 34) are formed by removing portions of the conductive layer on the one
10 side of a second segment of the circuit board, wherein the first and second segments of the circuit board are then mounted in parallel spaced relationship, and wherein shorting members are applied between the one end of the ground member and the one end of the first and second patches.

18. A dual-band microstrip antenna as in claim 12, wherein the antenna is formed from printed circuit board having a conductive layer on one side, wherein the conducting surfaces of the ground member are formed by
5 removing portions of the conductive layer on the one side of a first segment of the circuit board, wherein the conducting surfaces of the patch means (32, 34) are formed by removing portions of the conductive layer on the one
10 side of a second segment of the circuit board, wherein the first and second segments of the circuit board are then mounted in parallel spaced relationship, and wherein shorting members are applied between the one end of the ground member and the one end of the first and second patches.

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19. A dual-band microstrip antenna comprising at least two conductive radiating structures (32, 34) having electromagnetic interaction, at least one of the structures being apertured at locations where, if apertures were not present, induced currents would be relatively low compared to currents in other parts of the structure.

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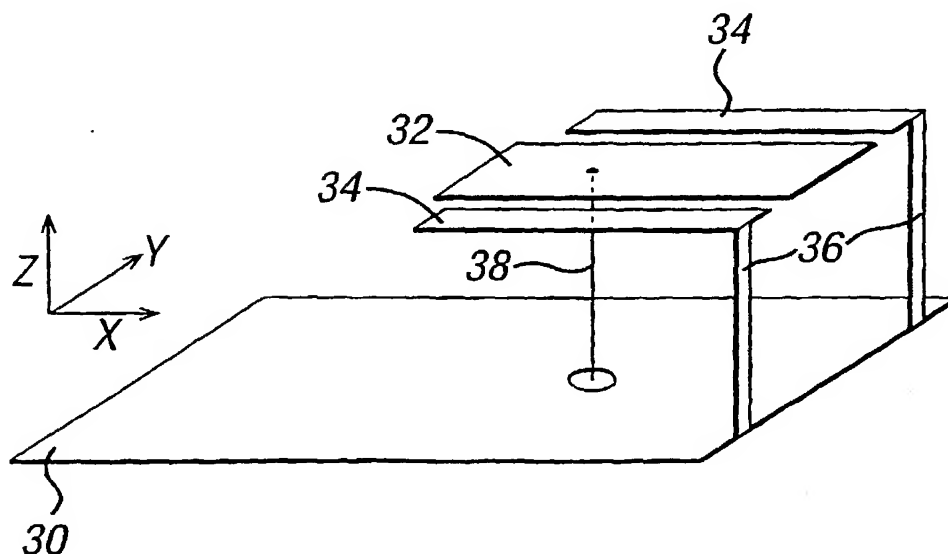
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(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZA, ZW.

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[Continued on next page]

(54) Title: DUAL-BAND MICROSTRIP ANTENNA



(57) Abstract: A dual-band microstrip antenna has a ground plate and also has a central patch positioned between a pair of side patches. The antenna has a single signal feedline, connected to the central patch, and the side patches are shorted to the ground element. Conductive surfaces of the ground plate and patches that carry surface current from signal radiation are contoured such that only portions of conductive surfaces that carry more than a negligible amount of the surface current are retained. The antenna has a reduced weight and improved bandwidth over conventional antennas that operate in the 925 MHz and 1800 MHz ranges.

WO 01/24314 A1

FIG. 1

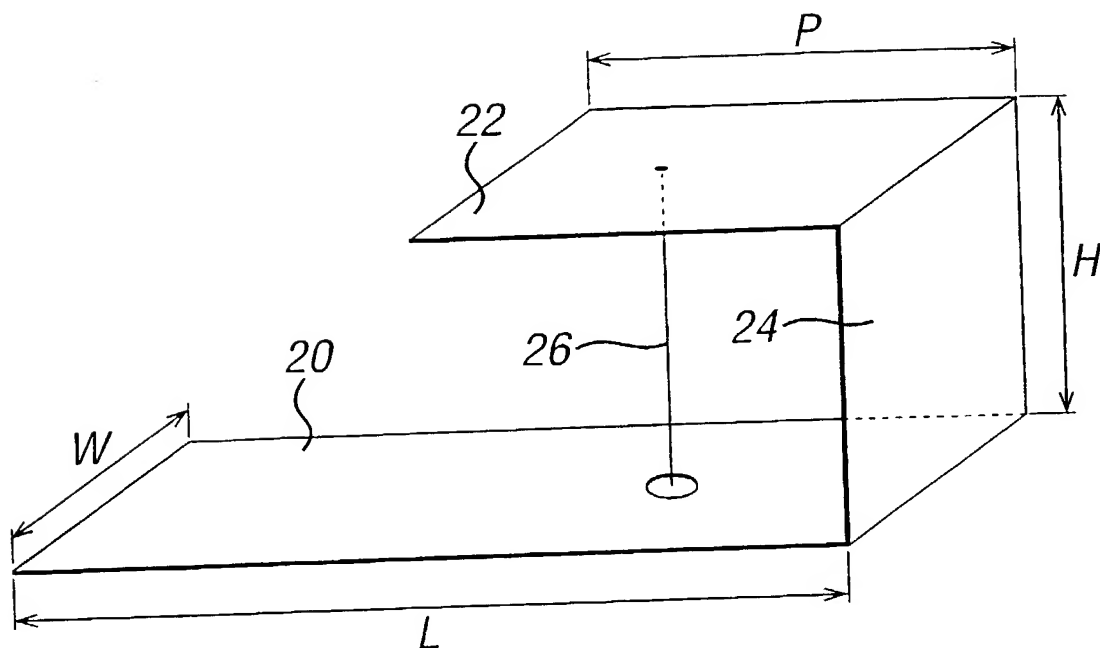
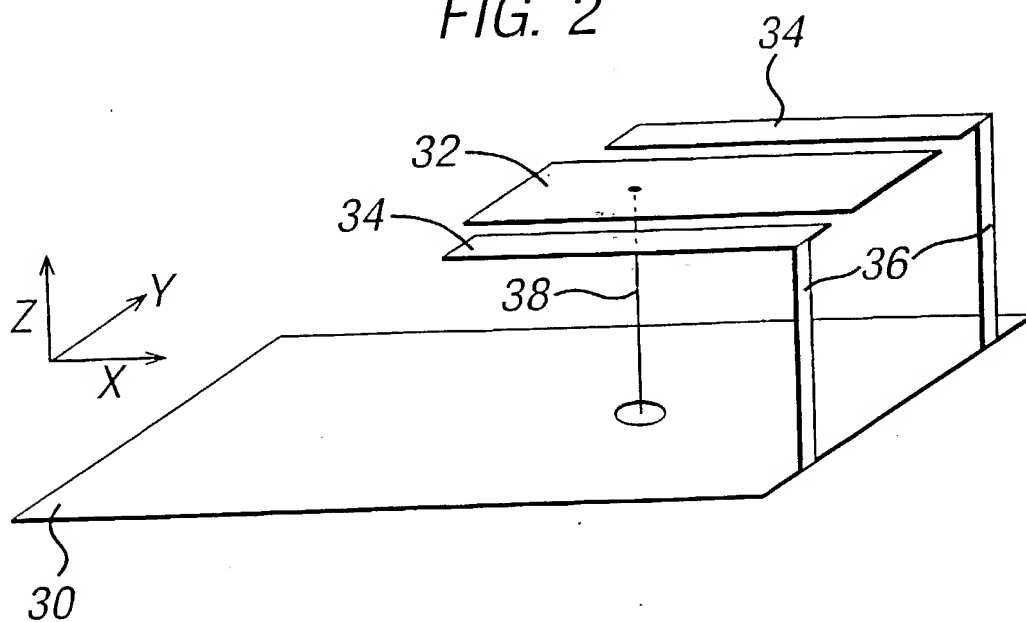


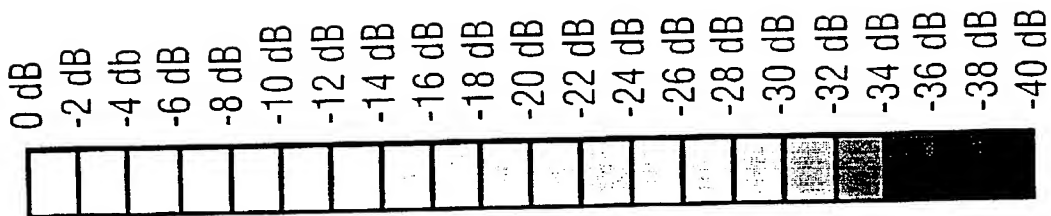
FIG. 2



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FREQUENCY: 0.925 [GHz]

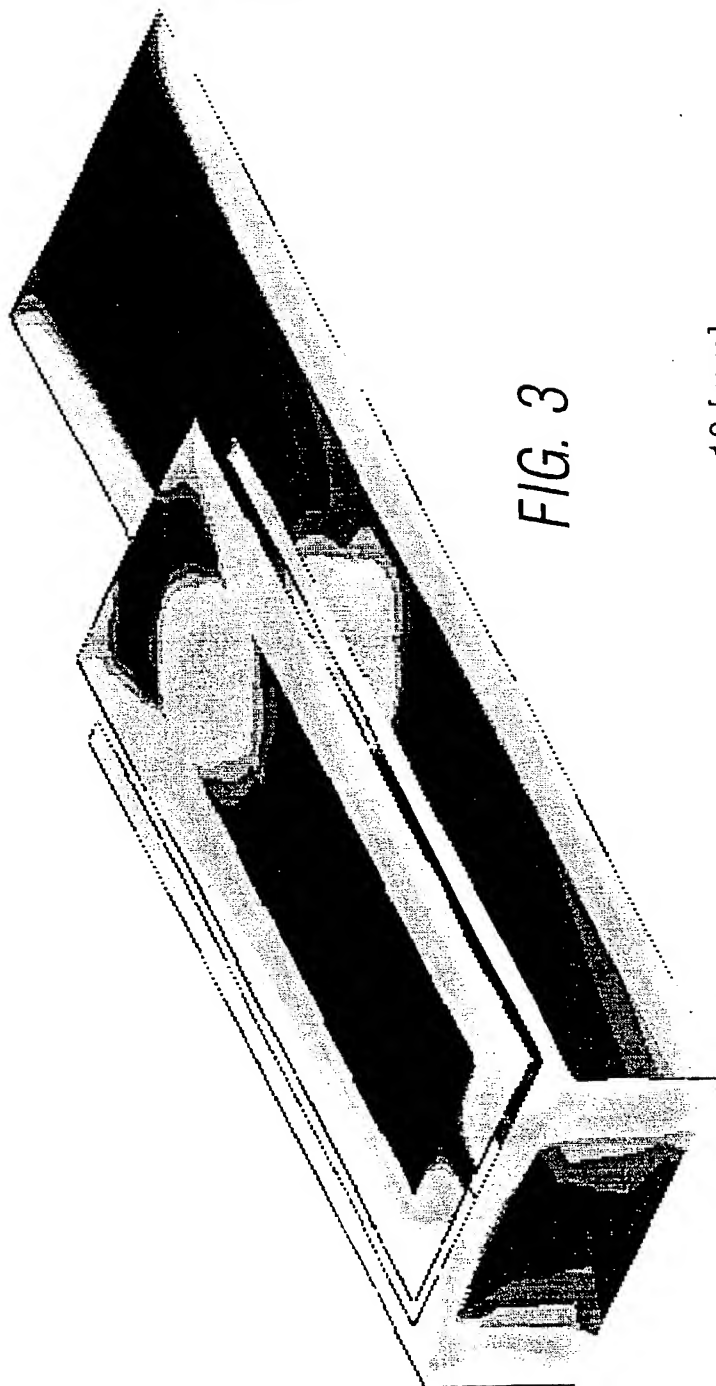
0 dB = 22.0354 [A/m]



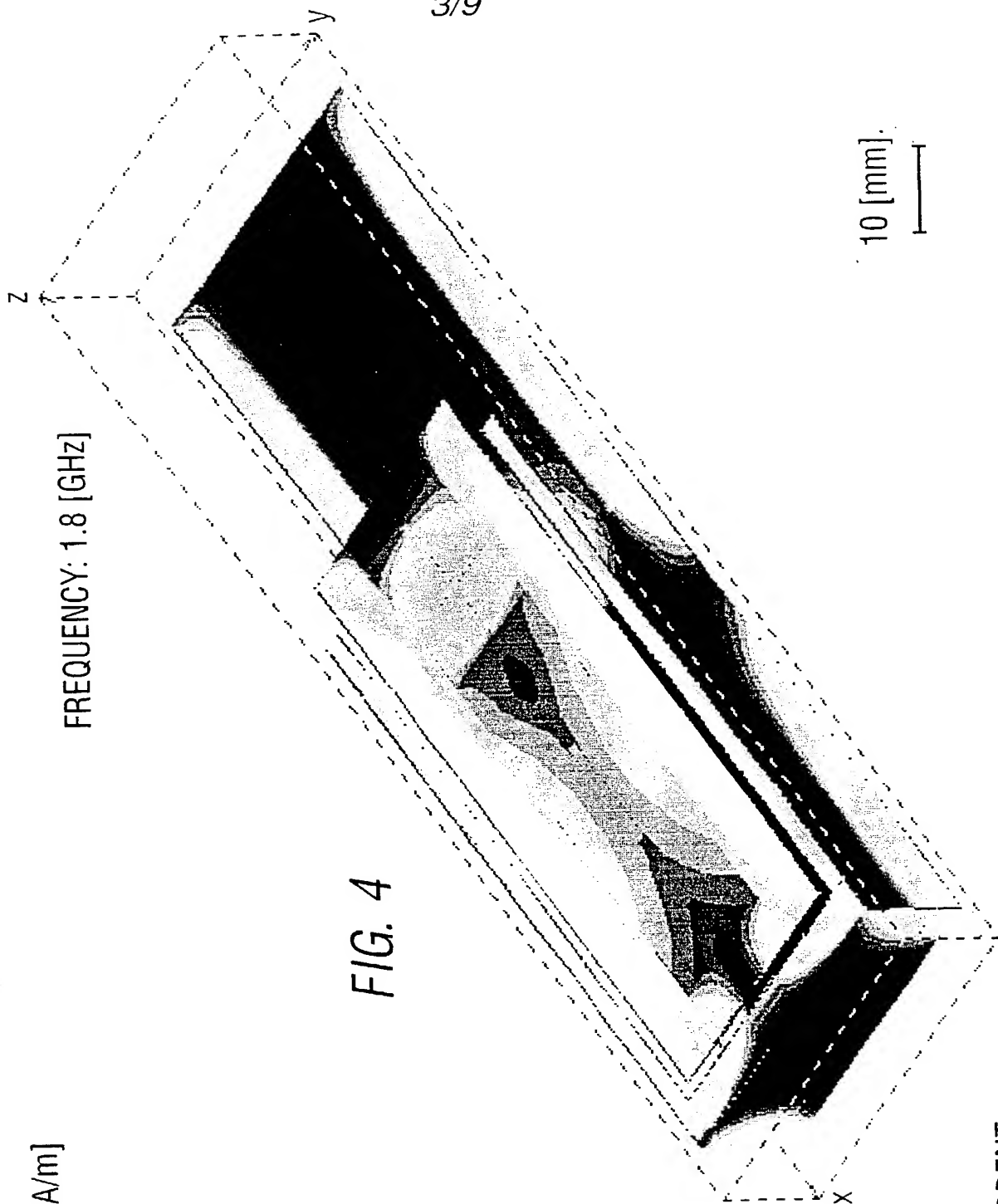
ELECTRIC CURRENT

FIG. 3

10 [mm]



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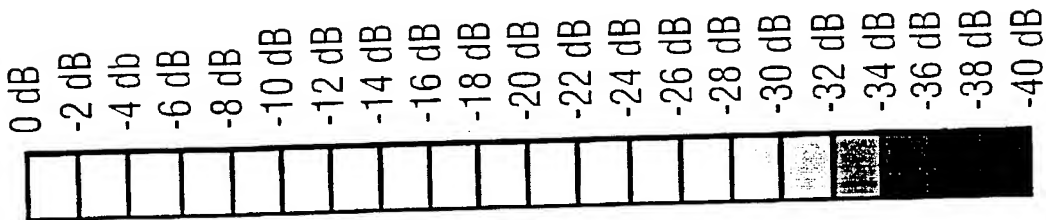


FREQUENCY: 1.8 [GHz]

10 [mm]

FIG. 4

0 dB = 26.2486 [A/m]



ELECTRIC CURRENT

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FIG. 5

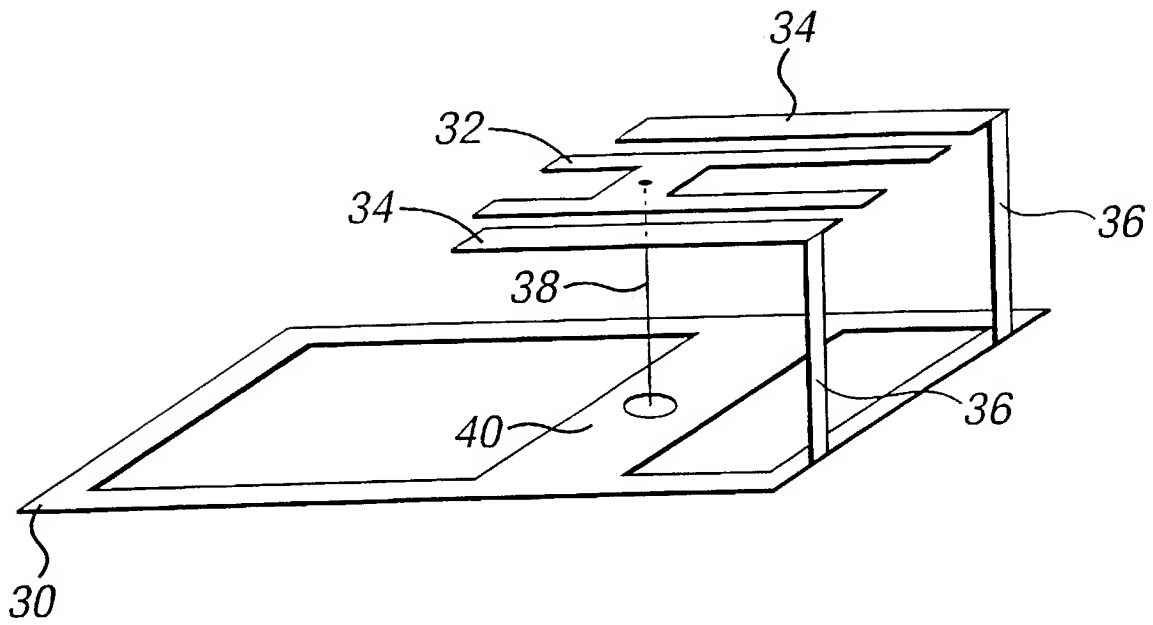
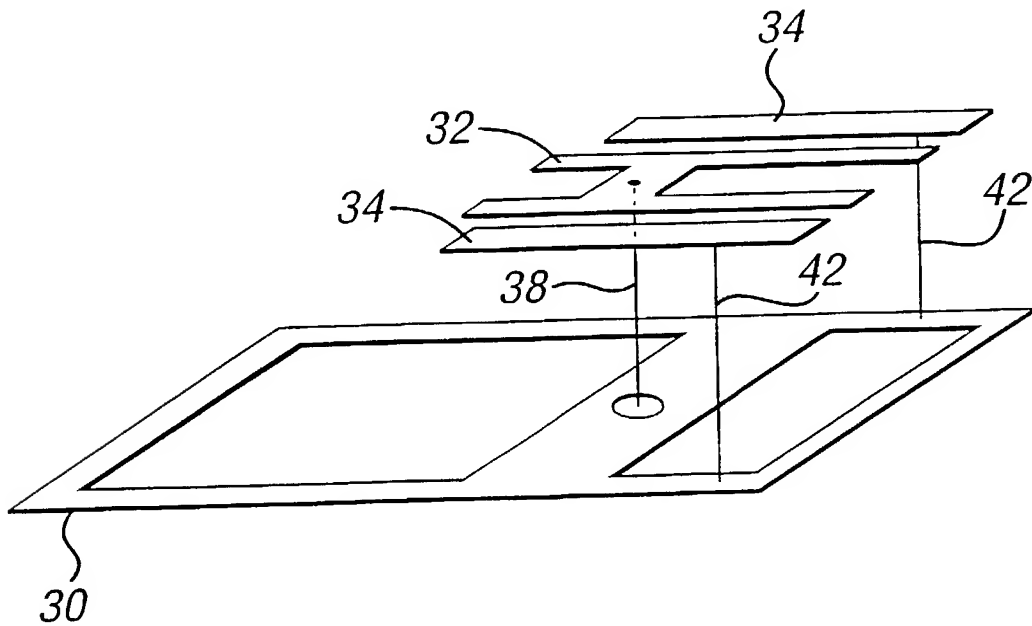
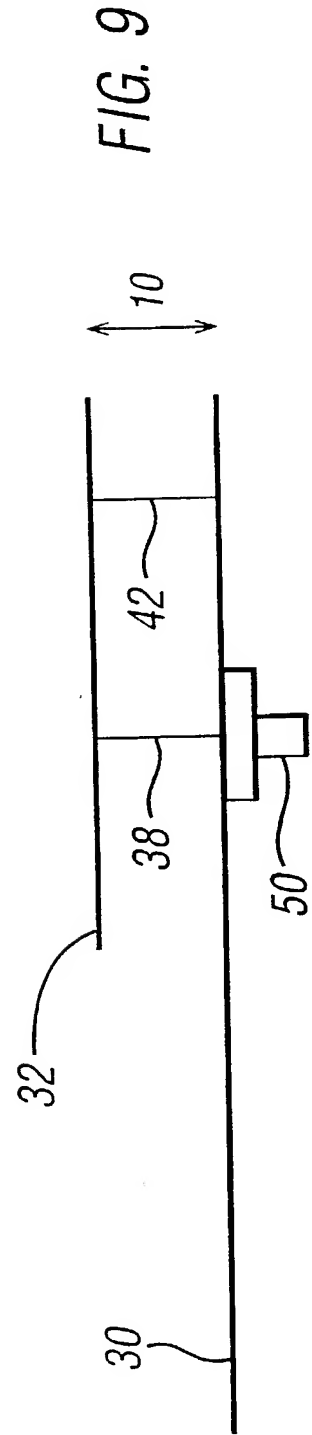
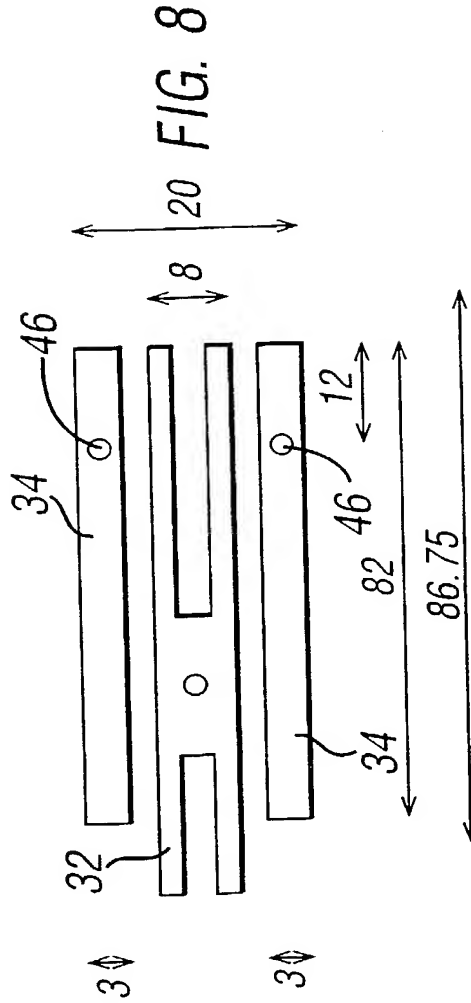
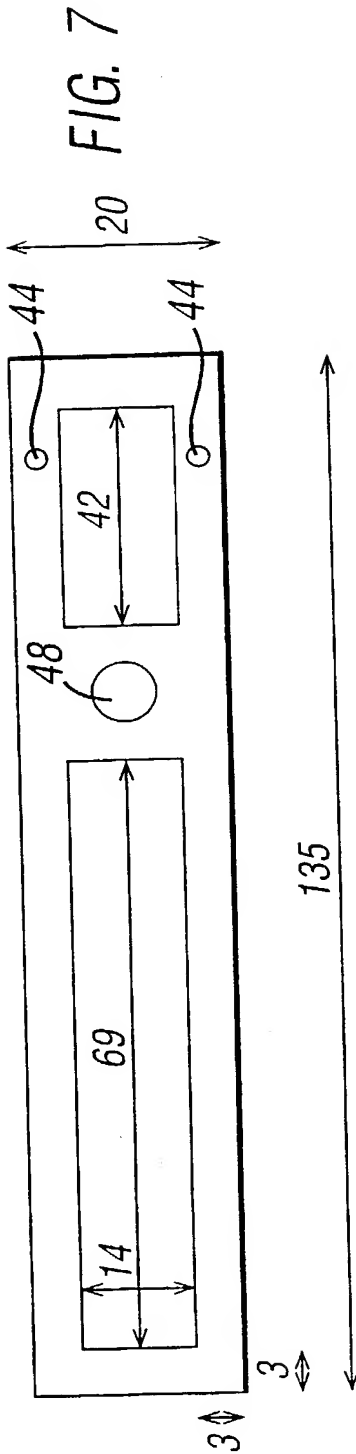
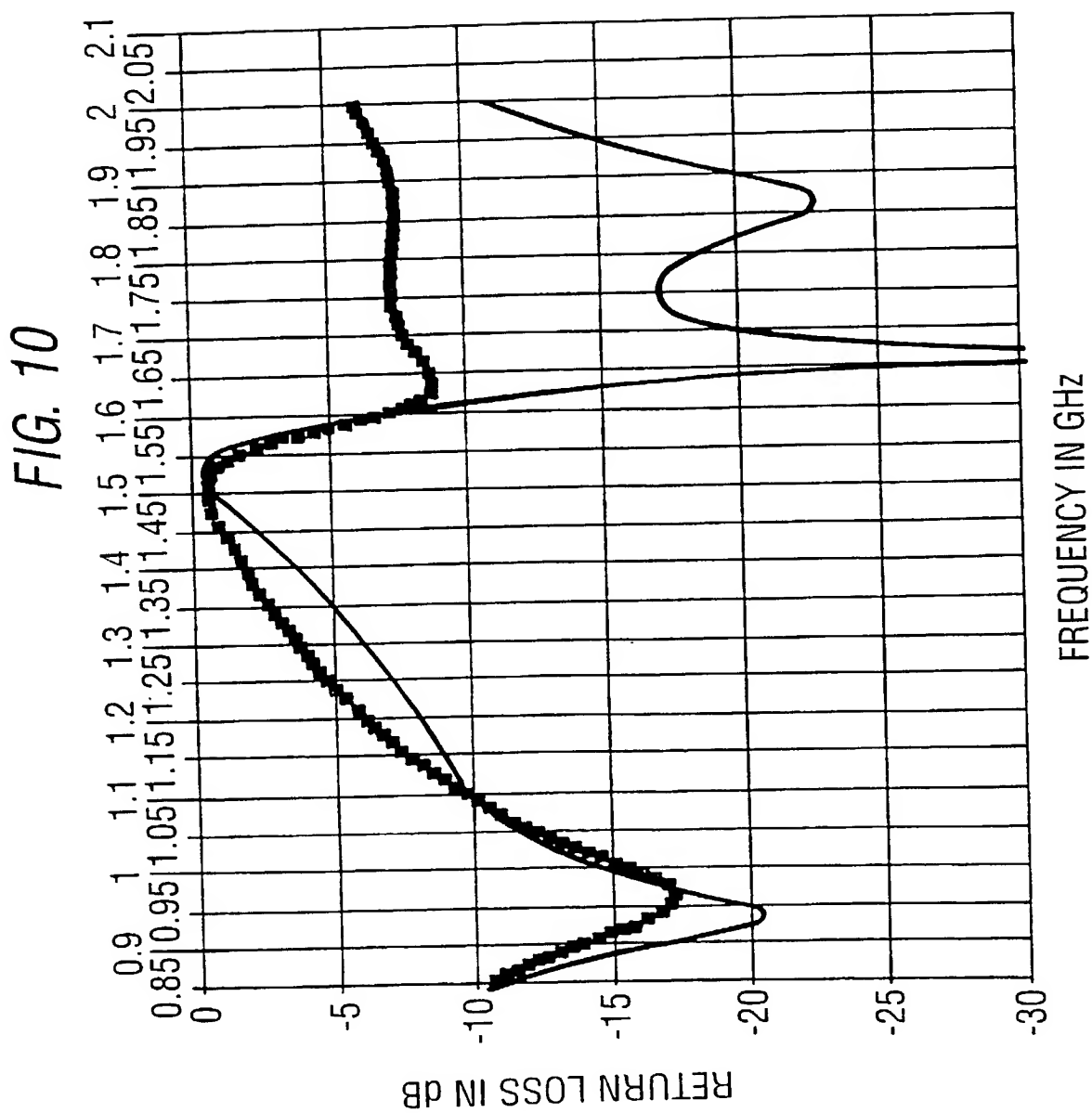


FIG. 6





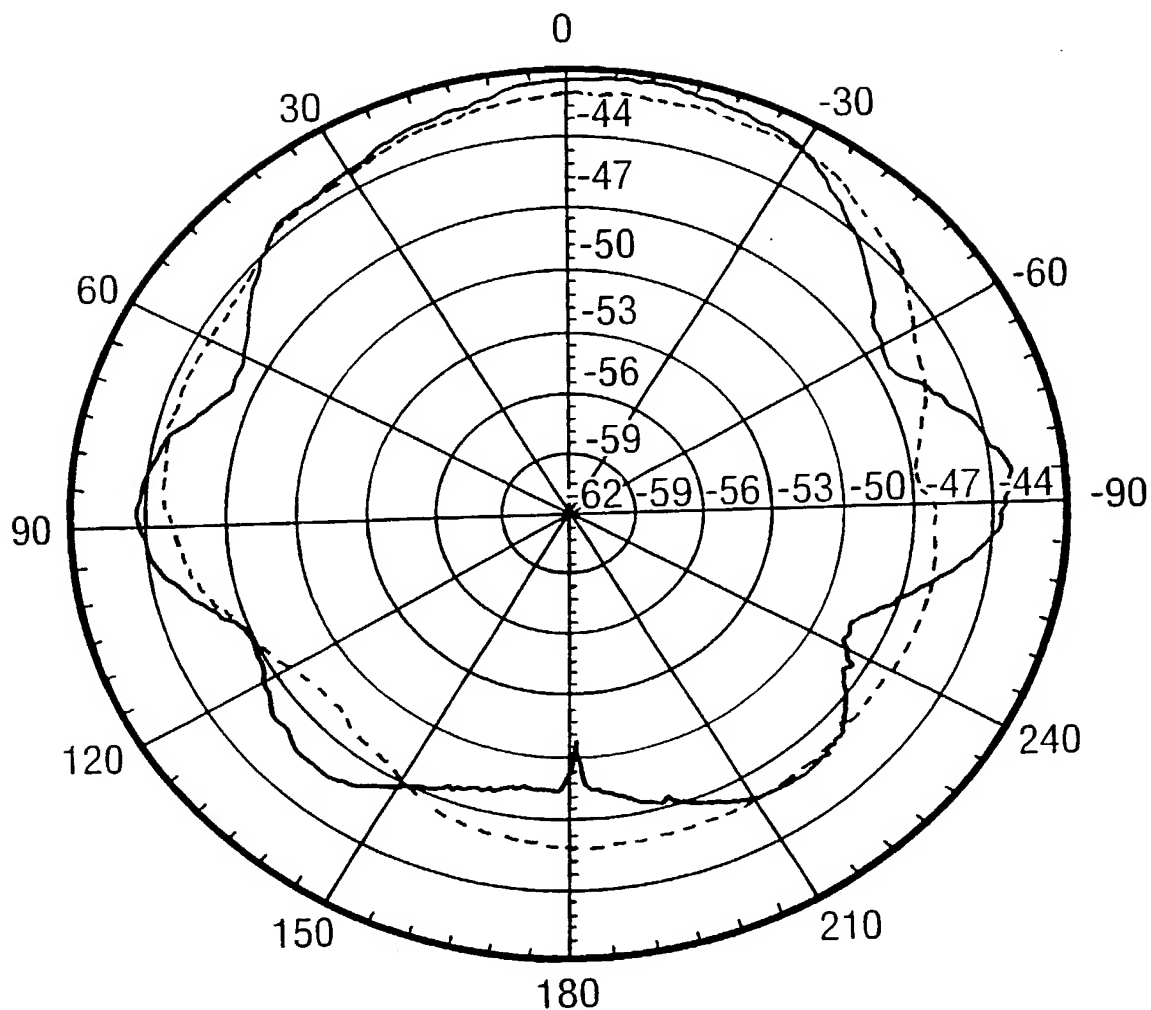
— WITH SLOTS
- - - WITHOUT SLOTS



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FIG. 11

PHI = 90 DEGREES



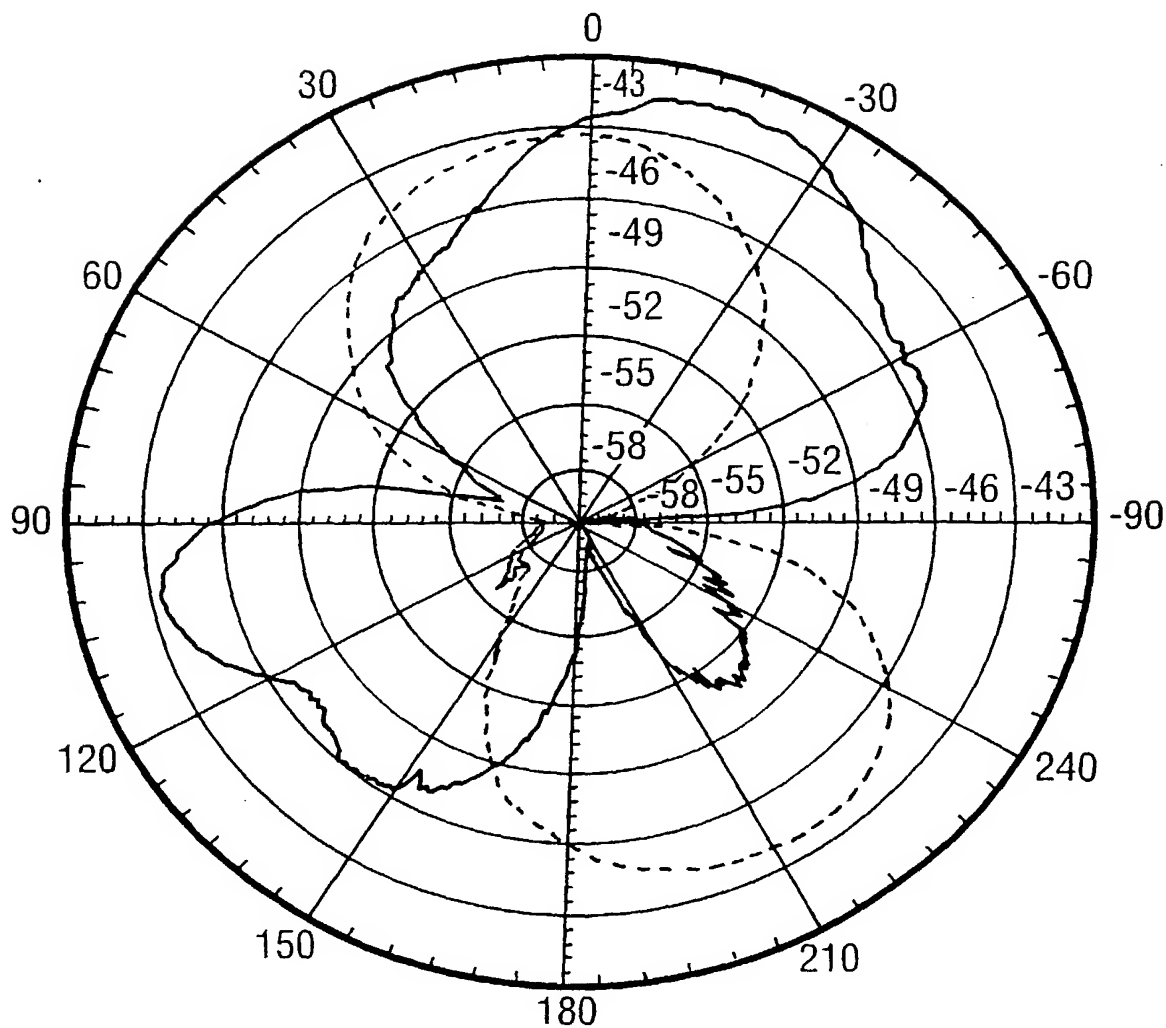
MEASURED RADIATION PATTERNS IN YZ PLANE

----- F = 925 MHz : COPOLARIZATION
——— F = 1.8 GHz : COPOLARIZATION

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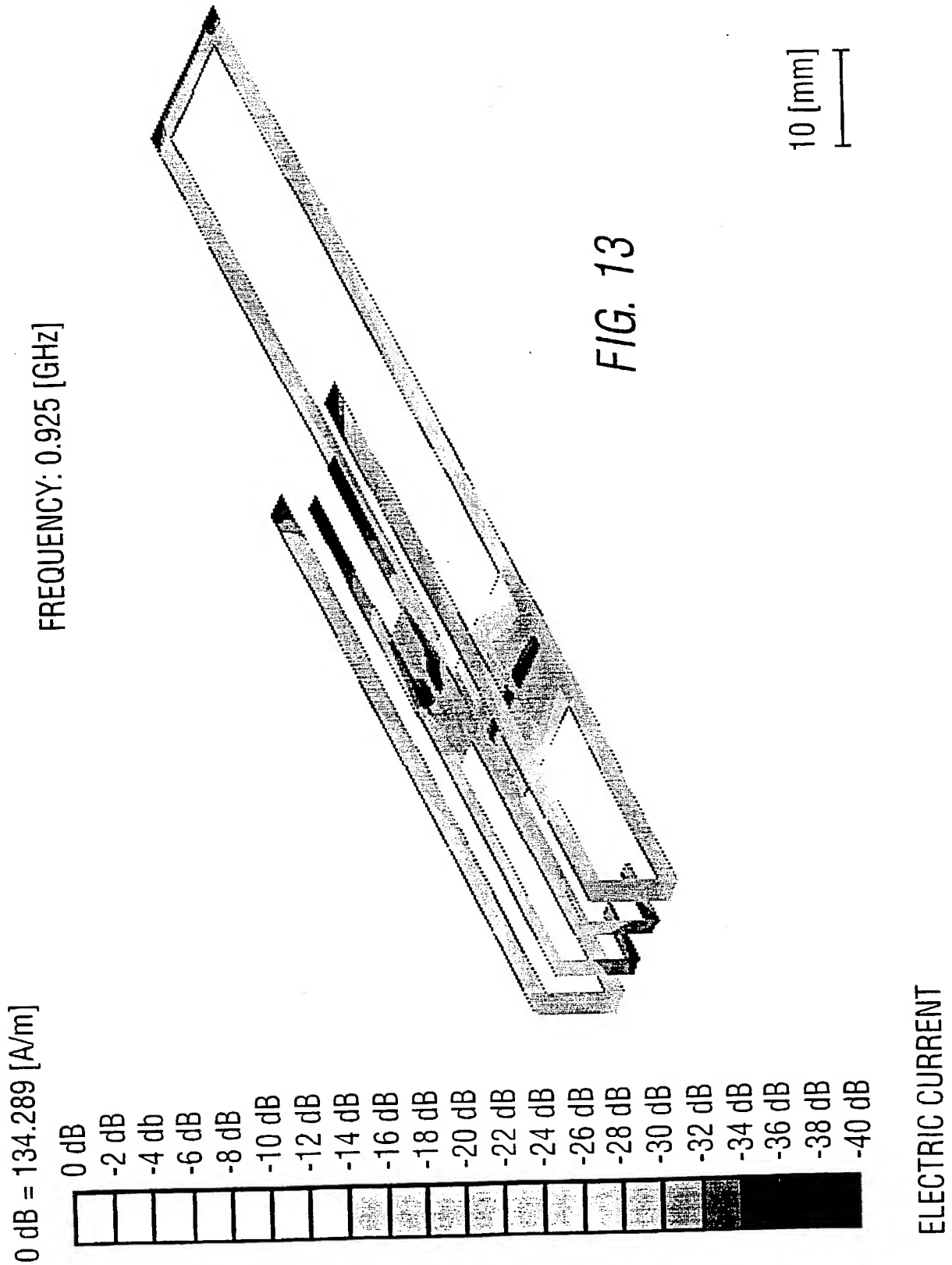
FIG. 12

PHI = 0 DEGREES



MEASURED RADIATION PATTERNS IN XZ PLANE

----- 925 MHz COPOLARIZATION
——— 1.8 GHz COPOLARIZATION



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**DECLARATION AND POWER OF ATTORNEY
(UNASSIGNED NONPROVISIONAL APPLICATION)**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below at 201 et seq. underneath my name.

I believe I am the original, first and sole inventor if only one name is listed at 201 below, or an original, first and joint inventor if plural names are listed at 201 et seq. below, of the subject matter which is claimed and for which a patent is sought on the invention entitled

Dual-Band Microstrip Antenna

and for which a patent application:

☐ is attached hereto and includes amendment(s) filed on

☒ was filed in the United States on 28/03/02 as Application No. 10/089,532

with amendment(s) filed on

☒ was filed as PCT international Application No. PCT/GB00/03746 on 29/09/00 and was amended under PCT Article 19 on

I hereby state that I have reviewed and understand the contents of the above identified application, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119(a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

EARLIEST FOREIGN APPLICATION(S), IF ANY, FILED PRIOR TO THE FILING DATE OF THE APPLICATION			
APPLICATION NUMBER	COUNTRY	DATE OF FILING (day, month, year)	PRIORITY CLAIMED
9923174.8	United Kingdom	30/09/99	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
			YES <input type="checkbox"/> NO <input type="checkbox"/>

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

APPLICATION NUMBER	FILING DATE

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

APPLICATION SERIAL NO.	FILING DATE	STATUS		
		PATENTED	PENDING	ABANDONED

POWER OF ATTORNEY: As a named inventor, I hereby appoint Benj A. Tetzan (Reg. No. 20060), David Wallé, III (Reg. No. 21094), Jonathan A. Marshall (Reg. No. 24614), Barry D. Rein (Reg. No. 22411), Stanton T. Lawrence, III (Reg. No. 25736), Charles E. McCarney (Reg. No. 22795), Phillip T. Shannon (Reg. No. 24278), Francis E. Morris (Reg. No. 24615), Charles E. Miller (Reg. No. 24576), Gidon D. Stern (Reg. No. 37469), John J. Lawler, Jr. (Reg. No. 27813), Brian M. Polzema (Reg. No. 28443), Brian D. Coppin (Reg. No. 27624), Rory J. Redding (Reg. No. 28749), Stephen J. Harbush (Reg. No. 29166), Donald J. Goodell (Reg. No. 19766), Thomas E. Friebe (Reg. No. 29258), Laura A. Conzatti (Reg. No. 30742), Jennifer Gordon (Reg. No. 30753), Geraldine F. Baldwin (Reg. No. 31232), Victor N. Balancia (Reg. No. 31211), Samuel B. Abrams (Reg. No. 30605), Steven I. Wallach (Reg. No. 33402), Marcia H. Summerson (Reg. No. 30893), Paul J. Ziegler (Reg. No. 35821), Edmond R. Bannan (Reg. No. 32110), Bruce J. Barlow (Reg. No. 33291), Adriane M. Aader (Reg. No. 32693), Thomas G. Rowan (Reg. No. 34419), James G. Murkey (Reg. No. 31636), Thomas D. Kohler (Reg. No. 32797), Scott D. Simpson (Reg. No. 33601), Gary S. Williams (Reg. No. 31066), Ann L. Gialoffi (Reg. No. 31956), Todd A. Wagner (Reg. No. 35399), Scott B. Familant (Reg. No. 35514), Kelly D. Talcott (Reg. No. 39582), Francis D. Corliss (Reg. No. 38100), Anthony M. Insigna (Reg. No. 35203), Brian M. Rothary (Reg. No. 35340), Brian D. Sitt (Reg. No. 35679), Michael J. Lyons (Reg. No. 37386), Candace T. Stephens (Reg. No. 37242), William J. Sipio (Reg. No. 34514), Nikolaos C. George (Reg. No. 39201), Stephen S. Rabinowitz (Reg. No. 40286), Ognjen V. Shonkov (Reg. No. 38051), and Kenneth L. Sehn (Reg. No. 38784), all of Pennie & Edmonds LLP, whose addresses are 1155 Avenue of the Americas, New York, New York 10036, 1467 K Street N.W., Washington, DC 20004 and 2300 Hillview Avenue, Palo Alto, Ca 94304, and each of them, my attorneys, to prosecute this application, and to transact all business in the Patent and Trademark Office connected therewith.

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		SIGNATURE OF INVENTOR		DATE	
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TOTAL P.05